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# Chip in the Tip ultra-compact cameras for endoscopic applications

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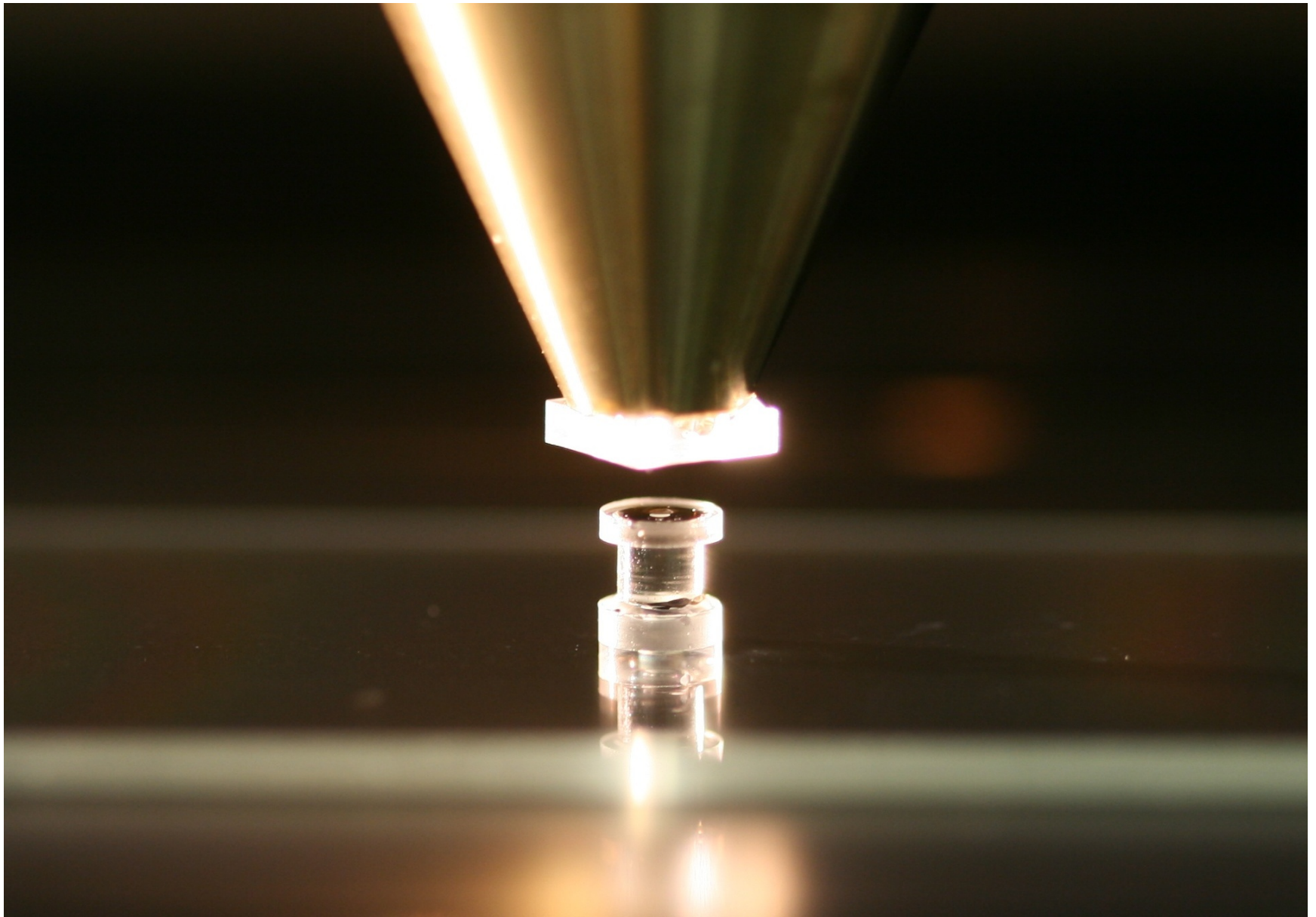
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<sup>4</sup>: *VIA electronic GmbH, Hermsdorf, Germany*

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MIXED-SIGNAL FOUNDRY EXPERTS

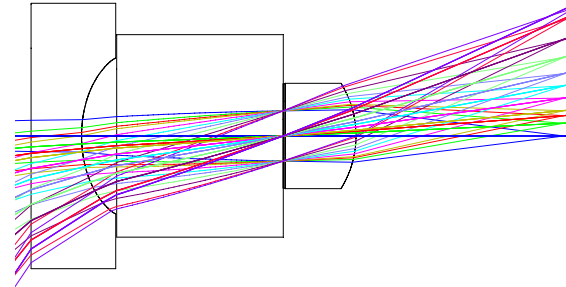
**Fraunhofer**  
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# Chip in the Tip – Motivation

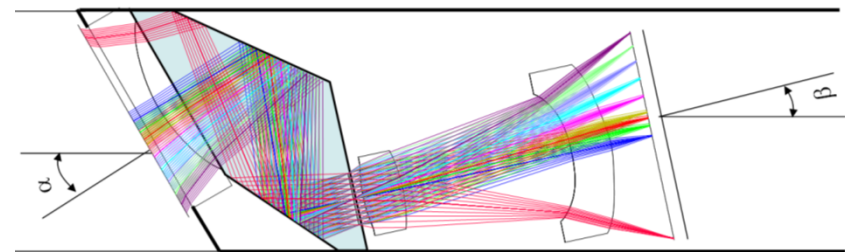
## Miniaturized Video Endoscope

- best possible resolution
- Image Acquisition and Processing Capacity in the endoscope Head
- Wire or Wireless Connection to the Environment
- compact Designs, round
- flexible Layouts and Assembly
- cheap Manufacturing
- fast Prototyping
- Stable or disposable Systems

*Straight View, 110 ° FOV*



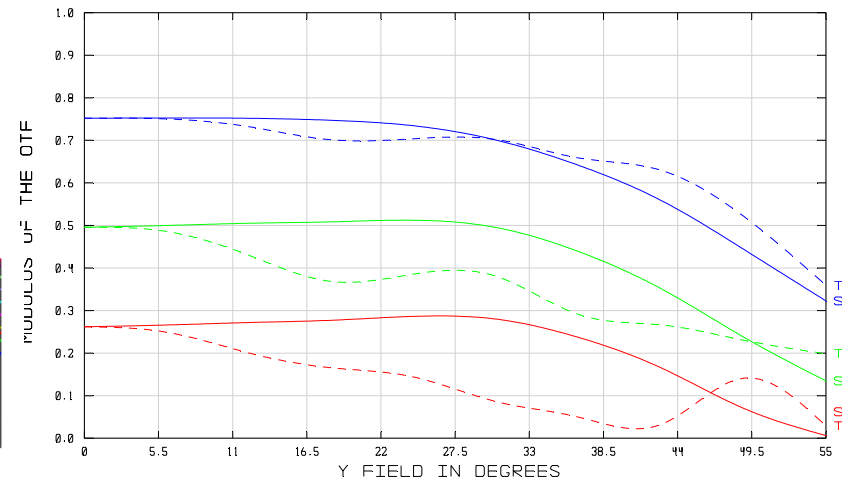
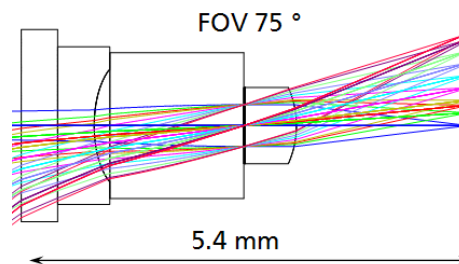
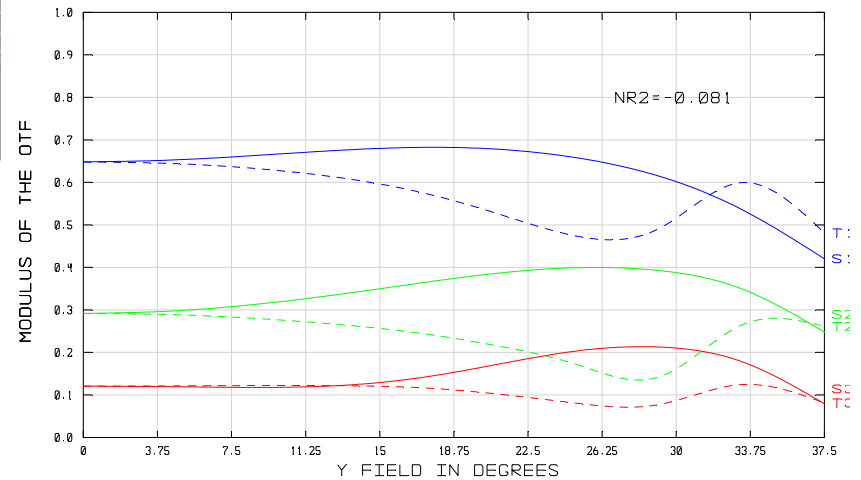
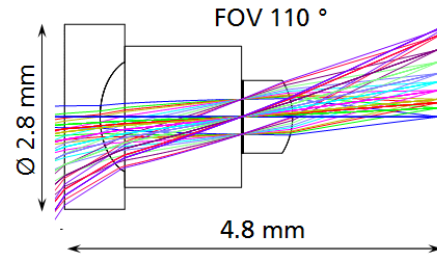
Objective + Imager



*30 ° View, 70 ° FOV*

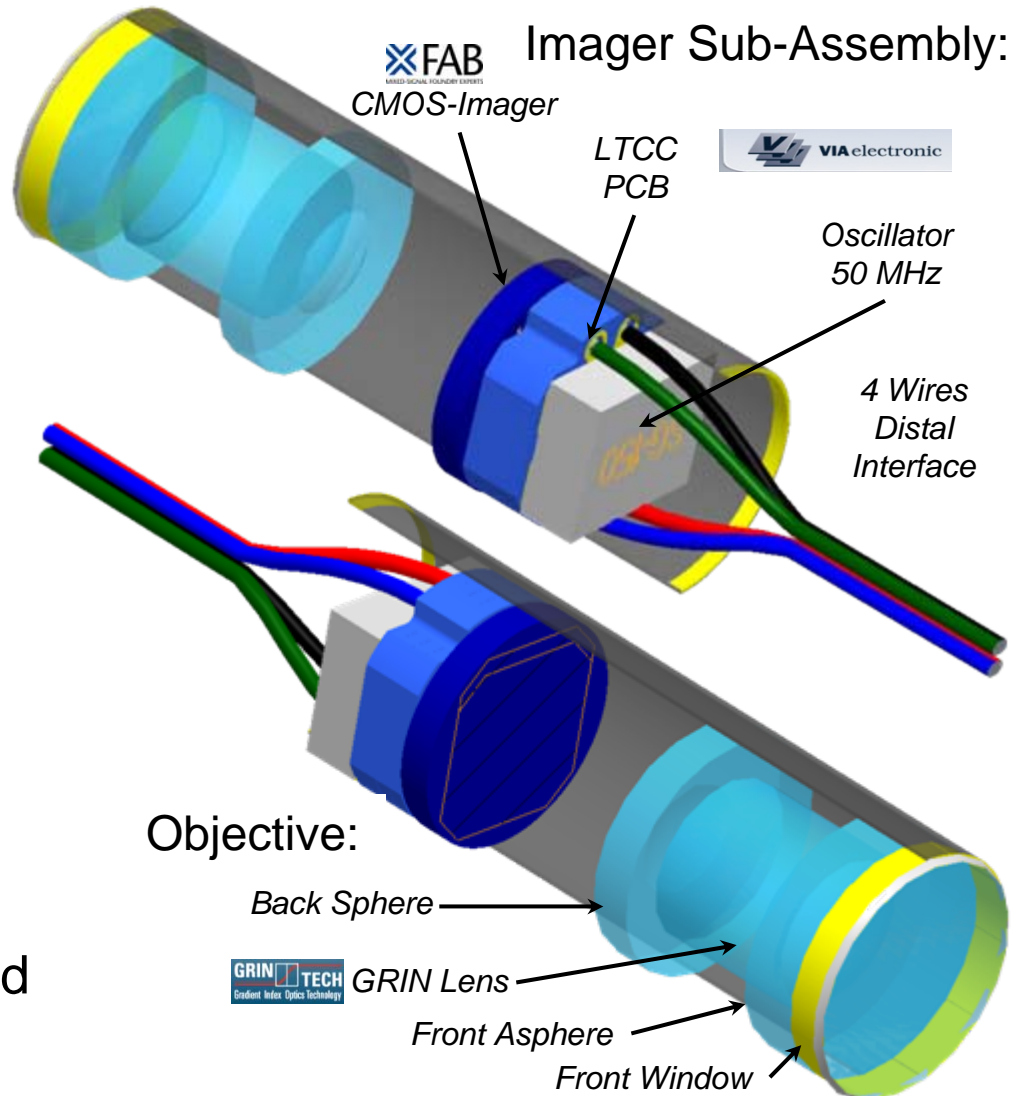
# Chip in the Tip – Optical Designs

- Straight View
- 2.8  $\mu\text{m}$  Pixel
- Nyquist 178 LP / mm
- FOV 75 ° and 110 °
- F-Number 4.6
- Object Distance 8 mm
- Front Plano-Asphere
- GRIN Lens with back side Aperture
- Back Sphere



# Chip in the Tip – Optomechanical Design

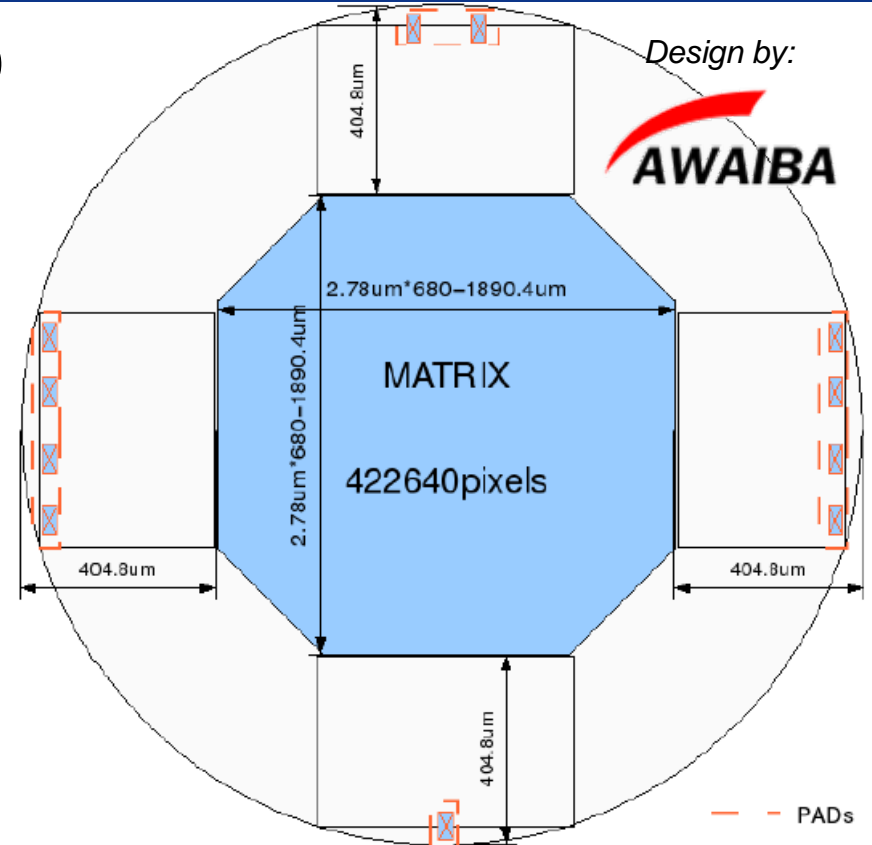
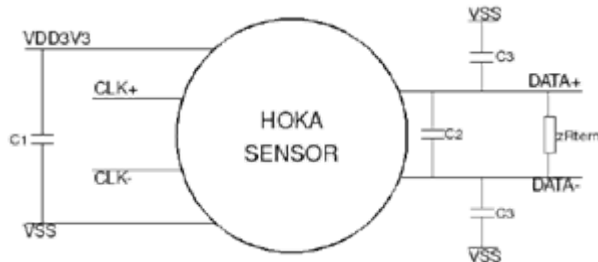
- Stainless Steel Tube  $\varnothing$  3 mm
- Objective  $\varnothing$  2.8 mm
  - SPDT / Injection moulded Lenses
  - Back Sphere: Drop-On-Demand possible
- stacked Objective
- Imager Anisotropic Conductive Adhesive mounted on LTCC Printed Circuit Board
- LTCC PCB carries Clock
- Imager connected during Assembly & actively aligned
- external Illumination



# VGA CMOS-Imager – Specifications



- >VGA<sup>(640x480)</sup> Resolution - 680x680
- Pixel Size 2.78  $\mu\text{m}$
- Image Circle  $\varnothing$  2.1 mm
- Outer  $\varnothing$  2.7 mm
- 50 Frames per Second
- On-Chip Pre-Processing
- 6 Connectors

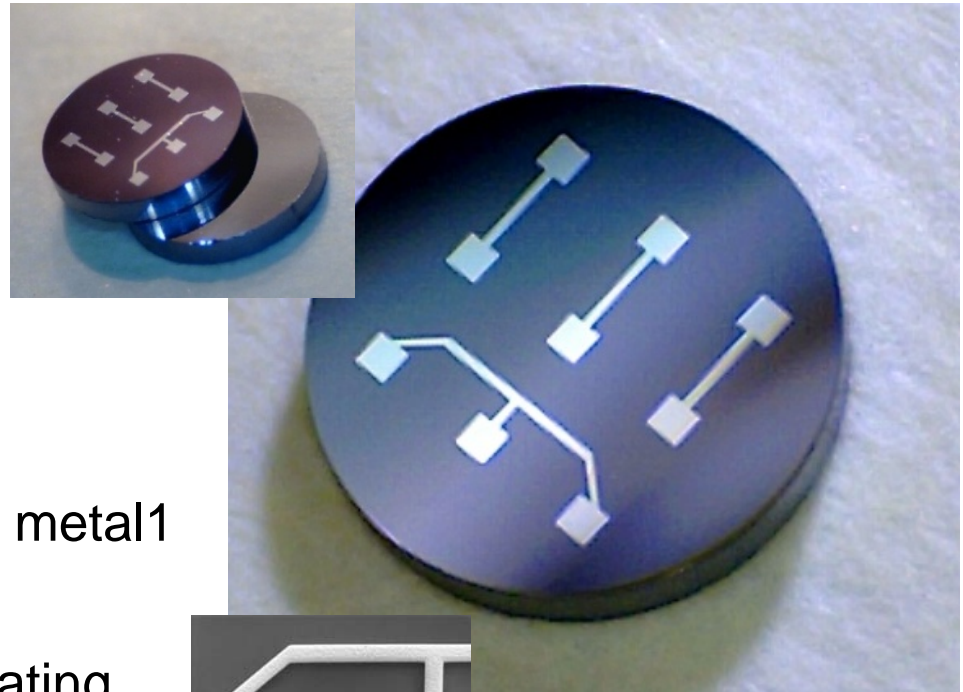


...because the world is analog.

# VGA CMOS-Imager – Through Silicon Via and FreeForm Dicing



- FreeForm Dicing - FFD
  - fully flexible chip shape
  - no unfunctional Si left
- Through Silicon Via - TSV
  - for 400  $\mu\text{m}$  wafers
  - TSV lands on  $60 \times 60 \mu\text{m}^2$  metal1 areas in CMOS Backend
  - Via filling by Cu electroplating
  - Backside pads and routing



*Backside Al-Routing on FFD Imager Chip*



*...because the world is analog.*

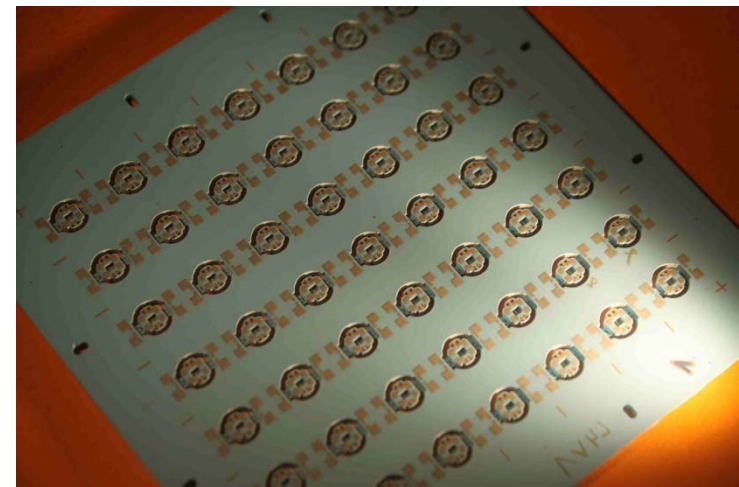
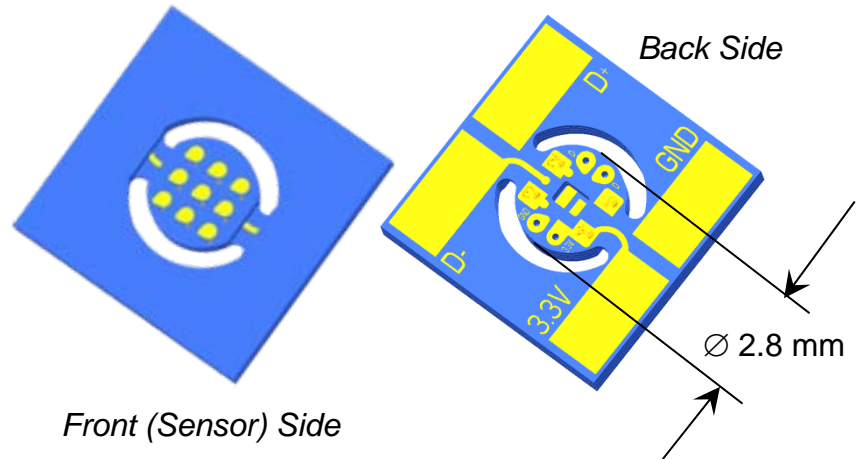
# Ceramic Carrier – LTCC PCB

## ■ Round LTCC Carrier

- $\varnothing$  2.8 mm, 0.8 mm thick
- 4 Layers DP951
- Rerouting Front (Sensor) -> Back (Interface) with Oscillator SG-150 (20..50 MHz) and C-Integration
- Out: Data +/-, 3.3 V, GND

## ■ 4" Multi-PCB Coupon

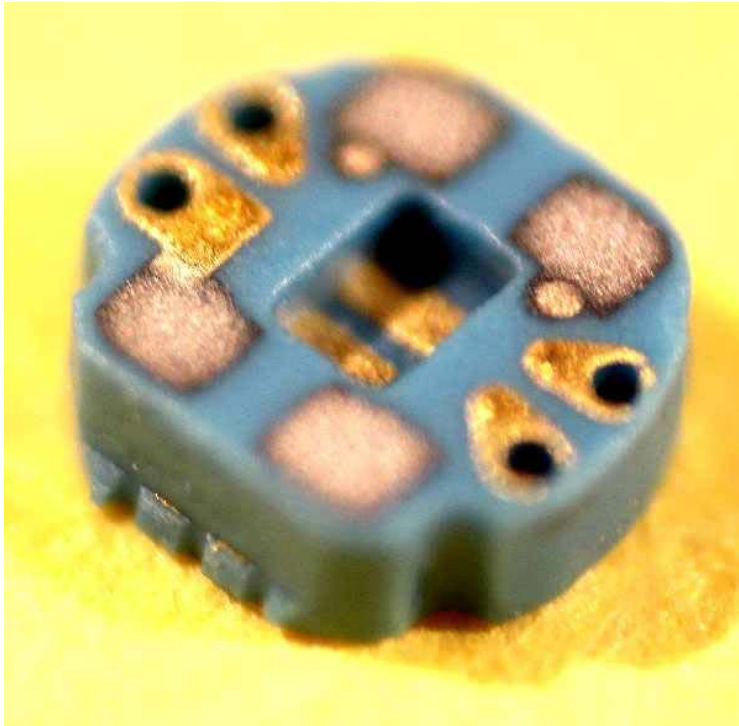
- 49 PCB per Coupon
- Pre-singulated by cutting / lasering
- Final Singulation after Reflow by Breaking



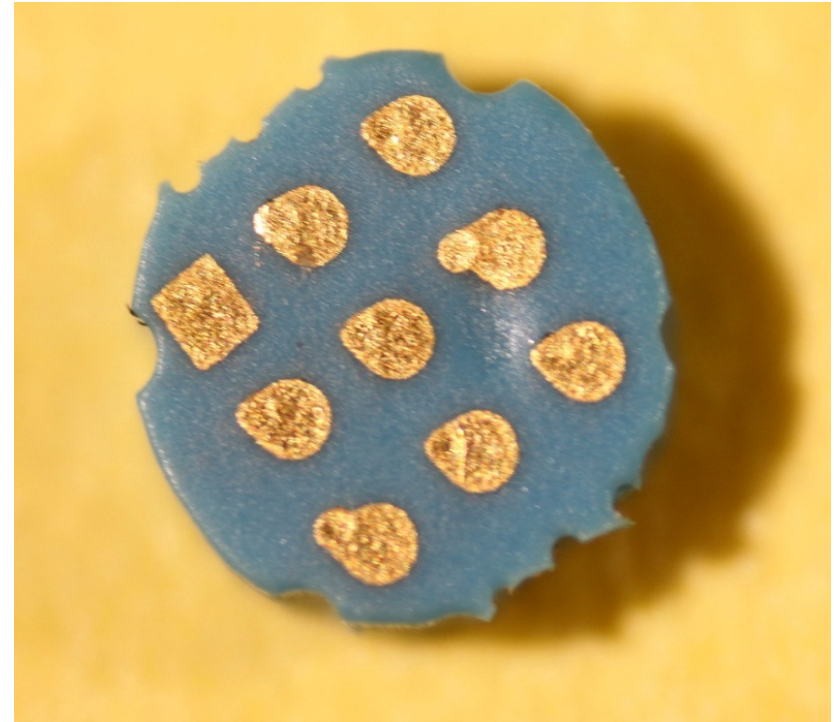
LTCC Coupon 4''



# Ceramic Carrier – LTCC PCB



*Back (Component and Interface) Side*

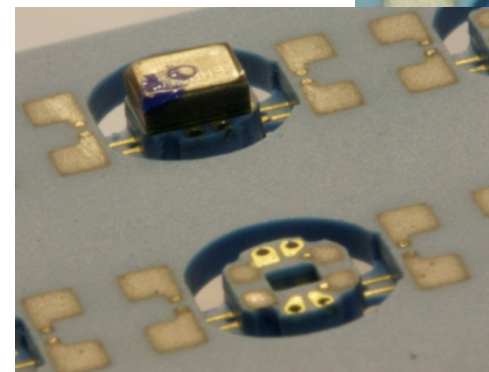
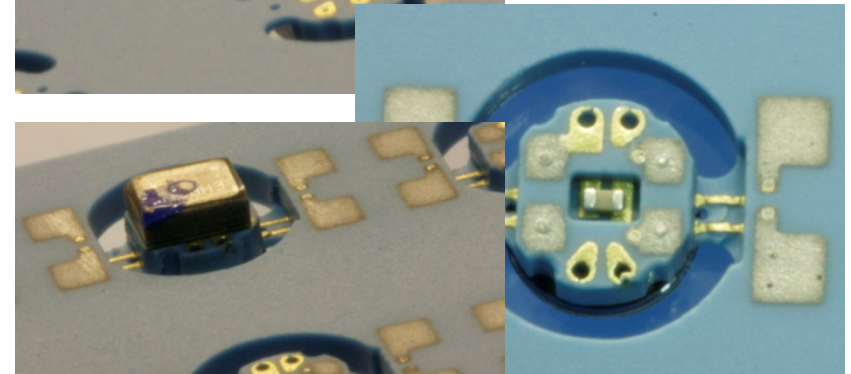
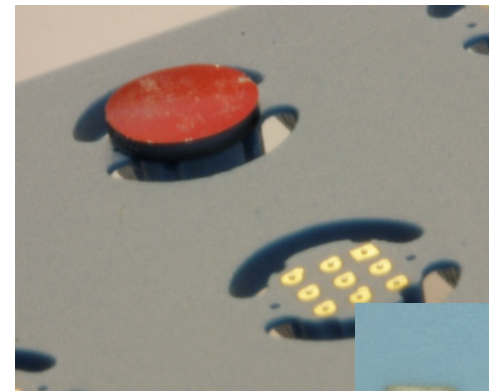
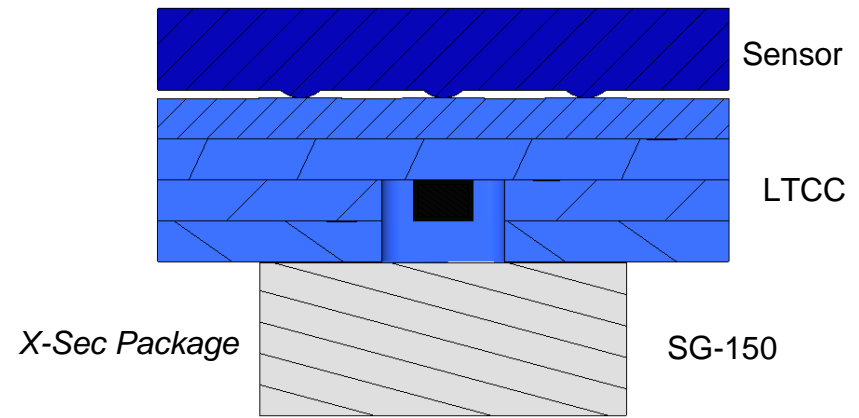


*Front (Sensor) Side*

# LTCC PCB Packaging

## ■ Two Stage Flip-Chip

- Sensor: Solder Bumps 200  $\mu\text{m}$ , 80Au20Sn on LTCC
- 370°C / 100°C Sensor / LTCC Reflow
- Components. Solder Bumps 400  $\mu\text{m}$  SAC305
- 240°C / 100°C Oscillator, C / LTCC
- Reflow by FiconTec BL2000



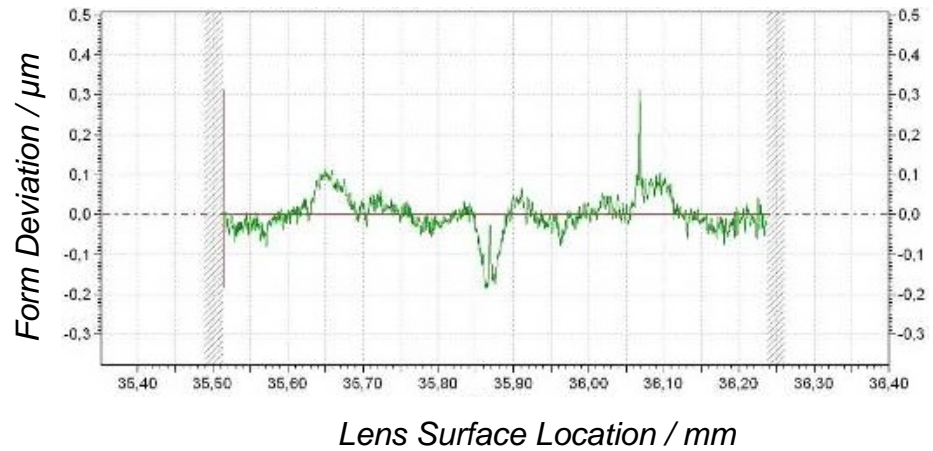
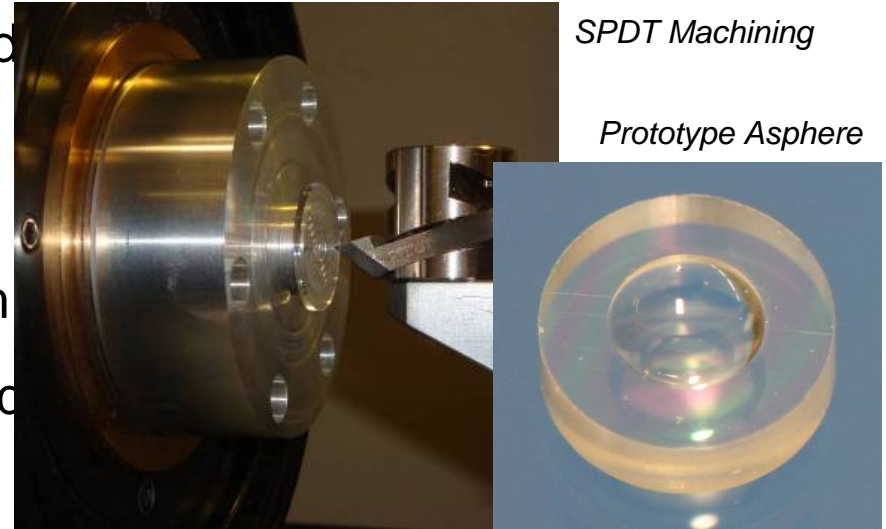
# Optics Manufacturing – Prototype Lenses SPDT

- Single Point Diamond Turning for Rapid Prototyping in Polymer

- Material: ZEONEX
- PV: <300 nm, rms ca. 50 nm

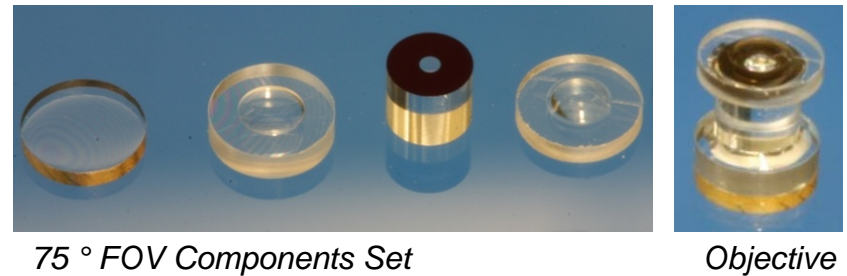
- Single Point Diamond Turning for Mould Manufacturing

- Mould Material: electro-plated NiP
- Ultrasonic Sound enhanced SPDT -> Stainless Steel Moulds
- Replication by Injection Moulding

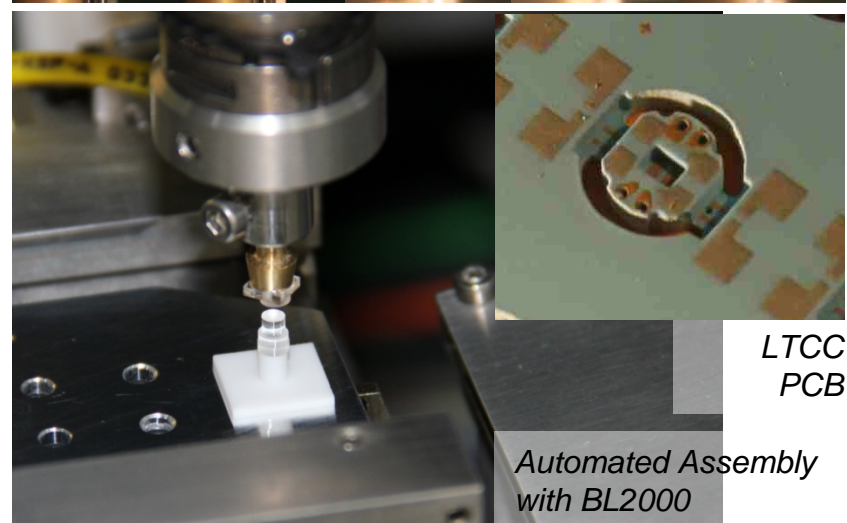
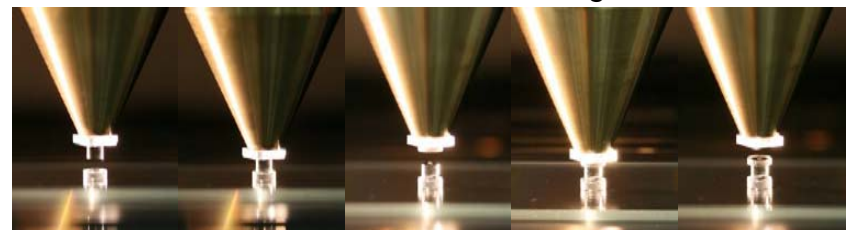


# Optics Assembly – Automated Pick&Place for Stacking

- Objective Stacking
  - passive Concentric Alignment
  - Decenter <math>< 10 \mu\text{m}</math>
  - Tip / Tilt <math>< 1^\circ</math>
- active Alignment (Tip/Tilt/ Focus)  
Objective <math>\leftrightarrow</math> Imager-Assembly
  - Imager pre-Assembly on LTCC
  - Handling / Connector frame on LTCC
  - Singulation @ predetermined break point
- automated in FlipChip Bonder BL2000 (ficonTEC)

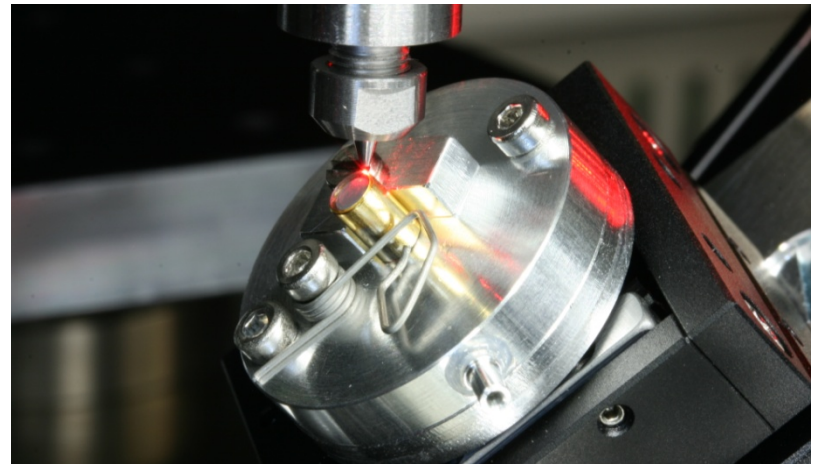
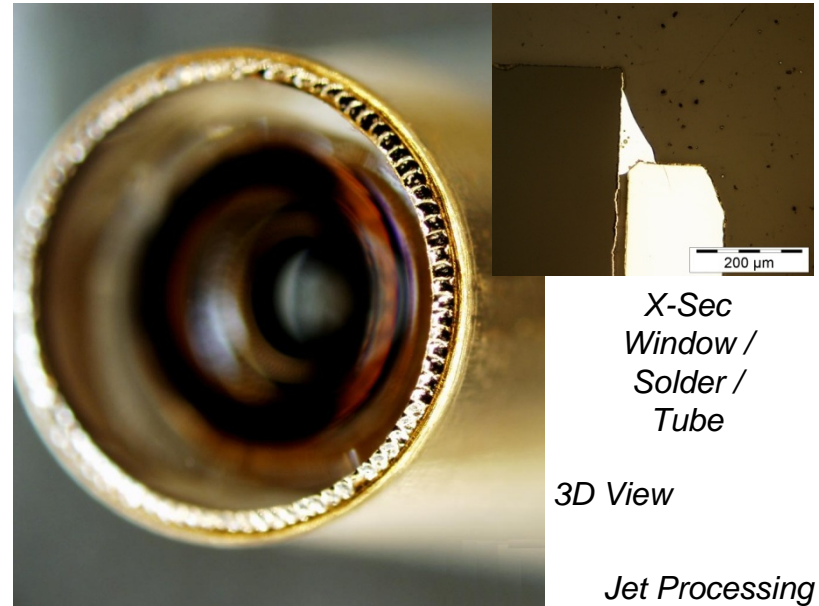


Stacking



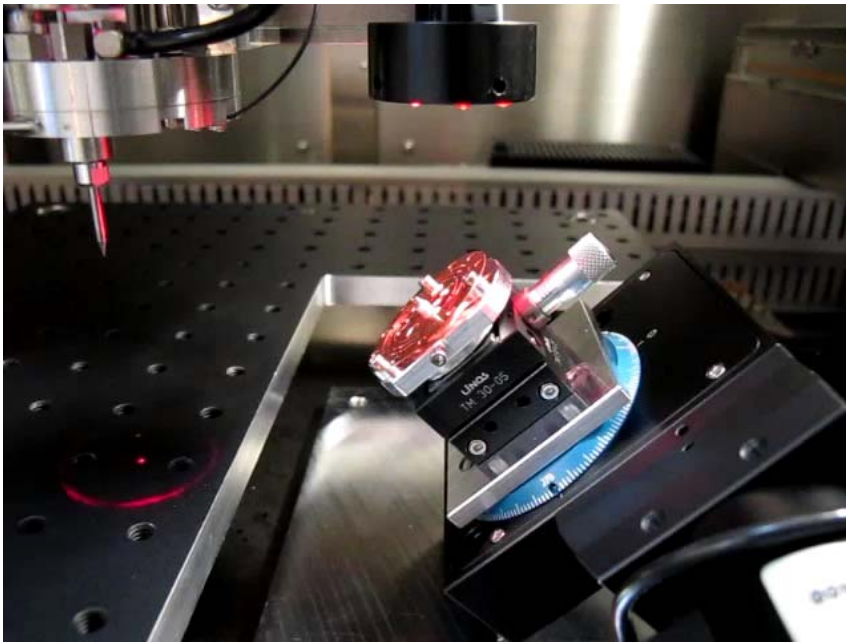
# Optics Assembly – Front Window Soldering

- Long term stable soldered Front Window
- Solderjet Bumping<sup>1</sup> – overlapping discrete Solder Droplets
- liquid Solder Application, no flux, local thermal Impact
- 300  $\mu\text{m}$   $\varnothing$  Spheres AuSn used
- Time ca. 1 min (Bumping Frequency 1 Hz, 10 Hz possible)
- Autoclave Cycles 5x @ 134 °C, 98 % rel. Humidity, 5 min
- Hermeticity before and after Autoclave Cycles:  $4\text{e}^{-9}$  mbar $\cdot\text{l}\cdot\text{s}^{-1}$

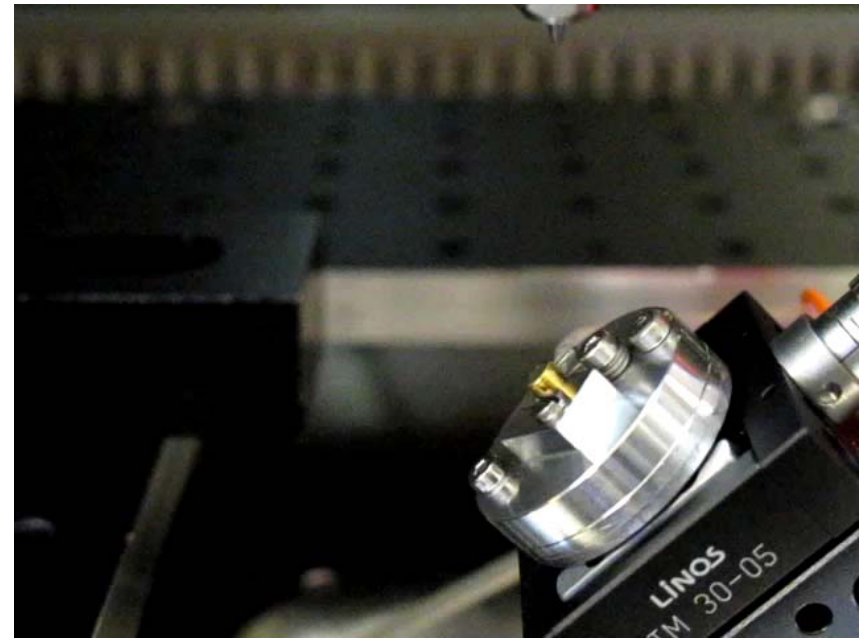


<sup>1</sup>: Patented: DE102007002436 & WO03/006197

# Front Window Soldering

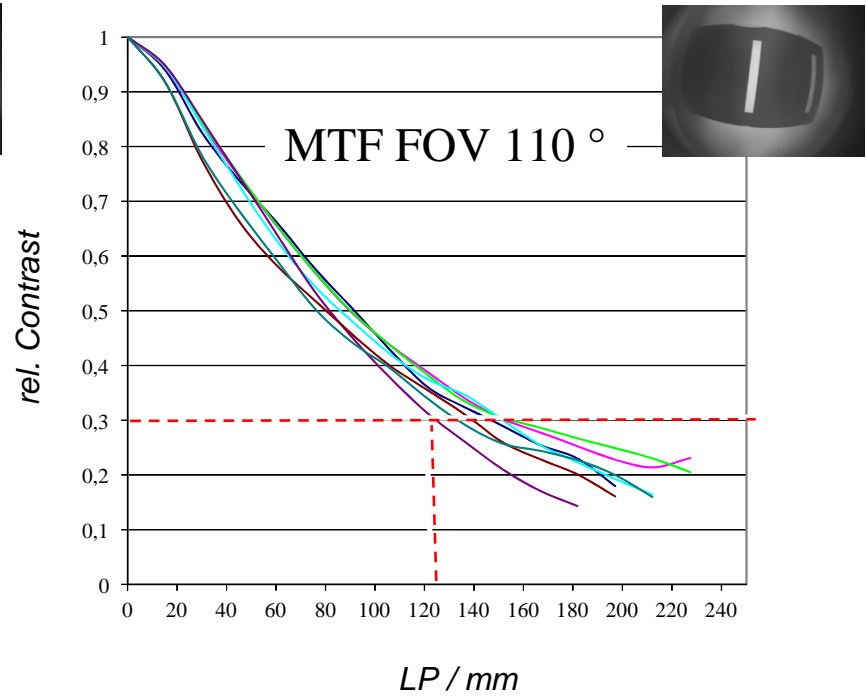
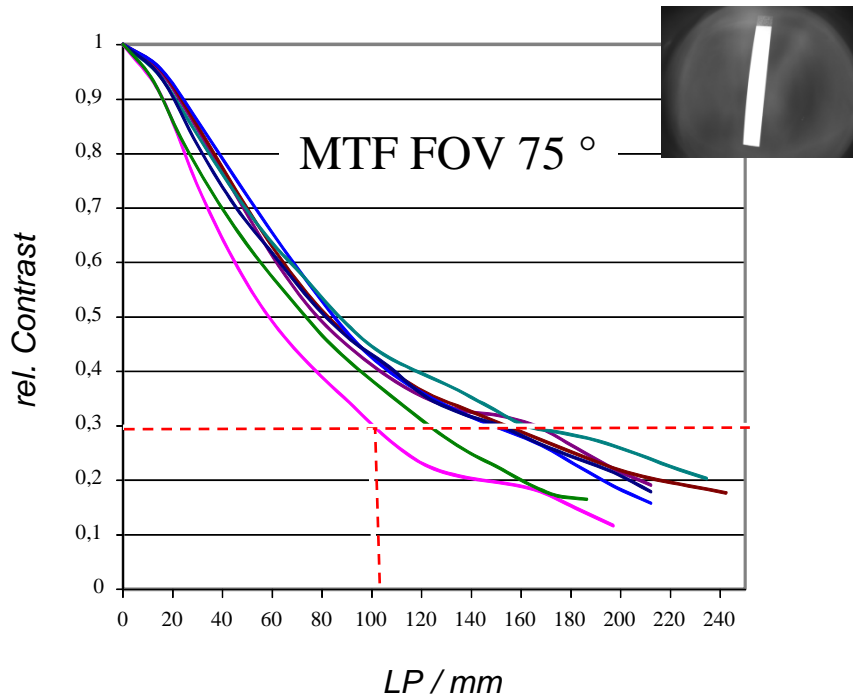


*First Step: Fixation*



*Second Step: Hermetic Sealing*

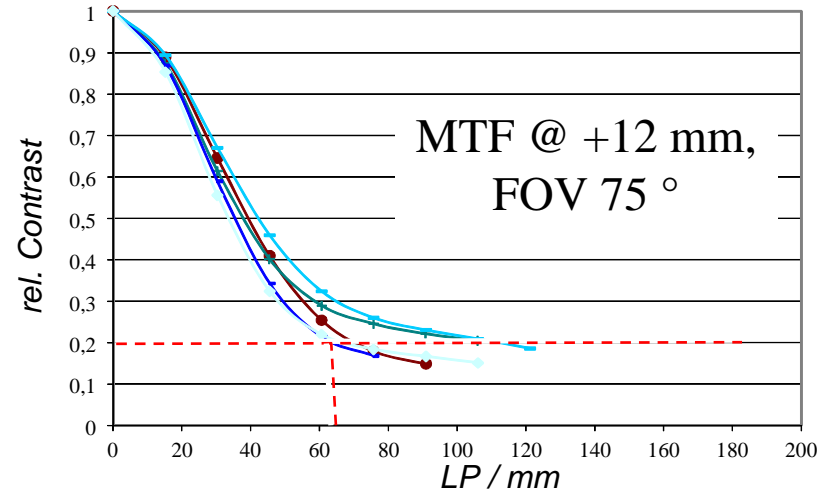
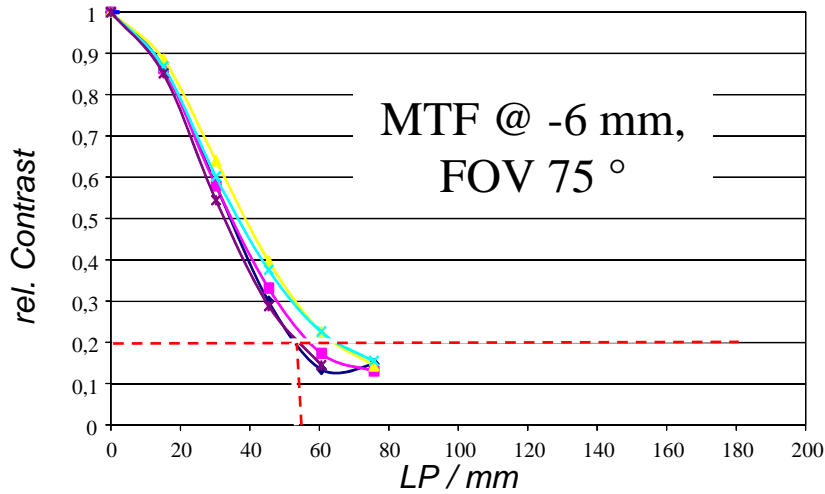
# Optics Evaluation – Modulation Transfer Function



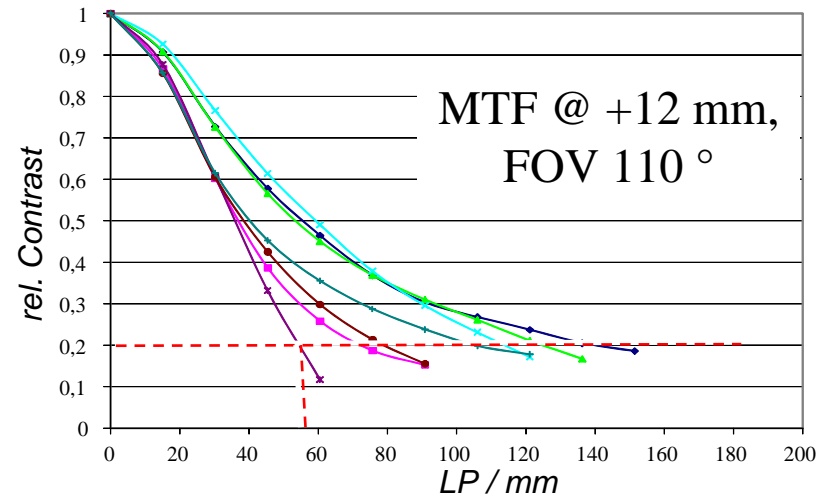
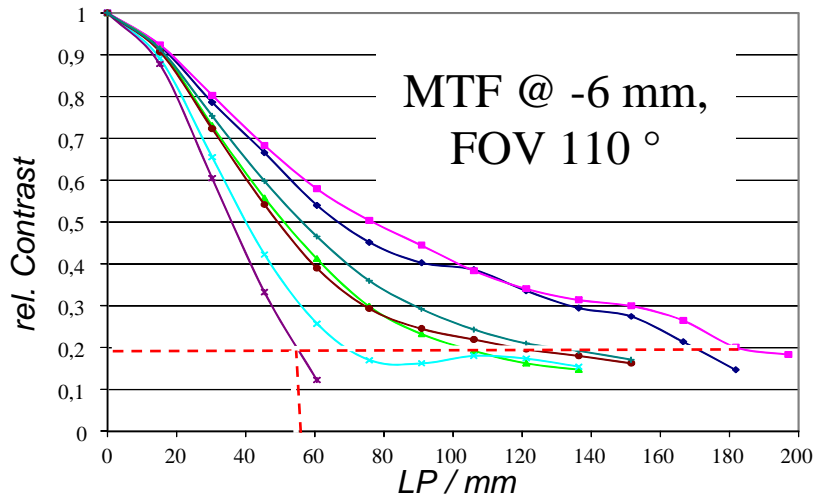
- Method: Slanted Edge\*, Goal<sub>(Nyquist/2)</sub>: 90 LP / mm @ 30 % contrast
- FOV 75 °: > 100 LP / mm @ 30 % contrast
- FOV 110 °: > 100 LP / mm @ 30 % contrast

\*: ISO/TC42N 4230 (1997), Electronic Still Picture Imaging. Spatial Frequency Response (SFR) Measurements

# Optics Evaluation – Depth of Fokus

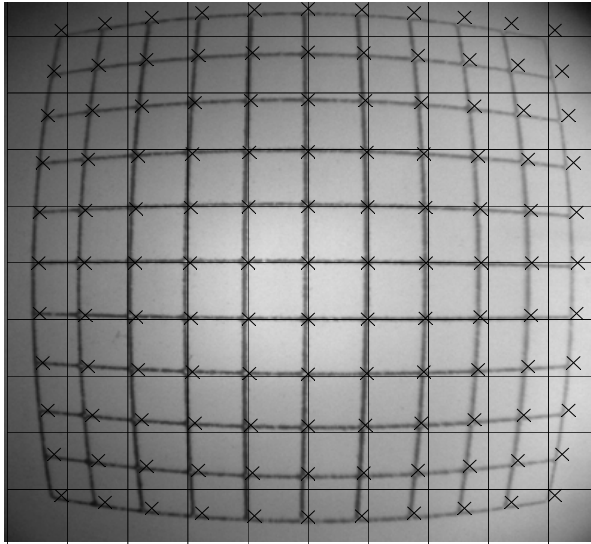


distal ← Goal: 60 LP / mm @ 20 % contrast → proximal



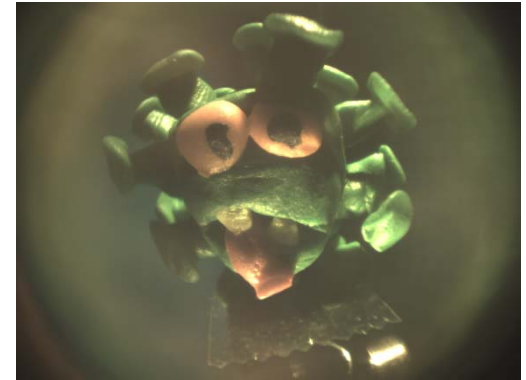


# Optics Evaluation – Distortion

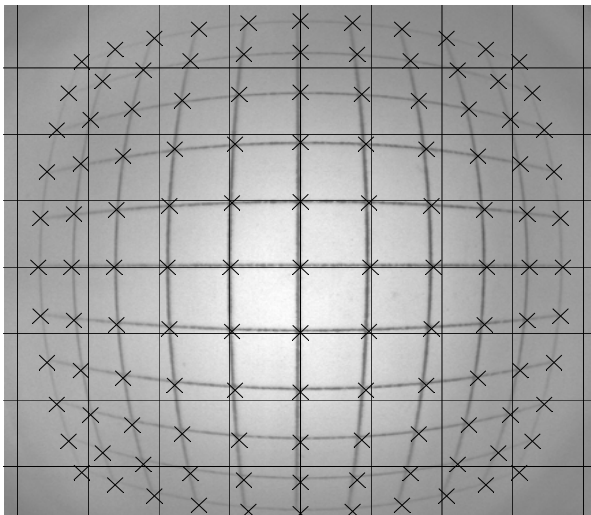


FOV 75 °

■ -18 % max.

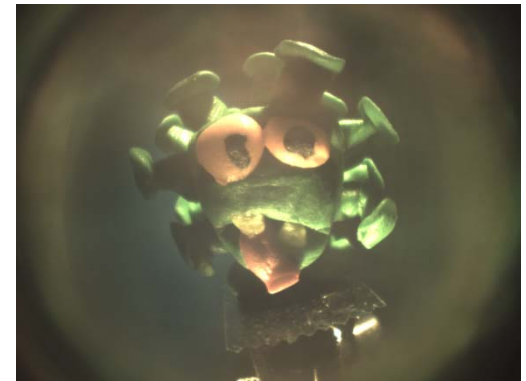


*scary „real life“ object*



FOV 110 °

■ -40 % max.

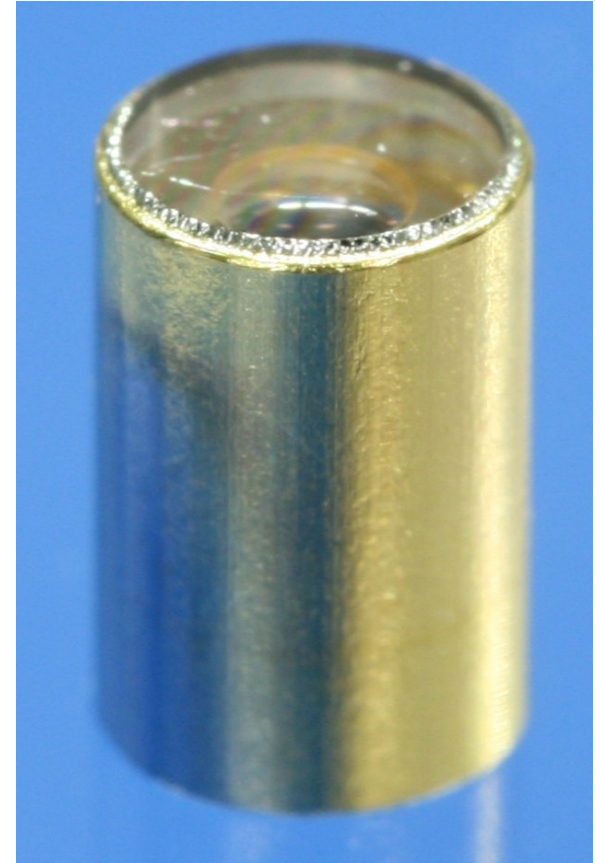


# Summary

- SPDT manufactured Prototype Lenses
- Central GRINS lens for stacking
- FFD round CMOS-Sensor with TSV
- Pick& Place for simplified Assembly
- long term stable soldered Front Window
- automated mid-Volume Production



Flexible Integration



# Acknowledgement

The work presented in this presentation is based on the project HoKa (miniaturized high resolution camera systems). Funding by the Thuringian Government and the European Regional Development Fund (ERDF) as well as project management by the Thueringer Aufbaubank (project number 2008 VF 0057) is gratefully acknowledged.



Thank you for your Attention.